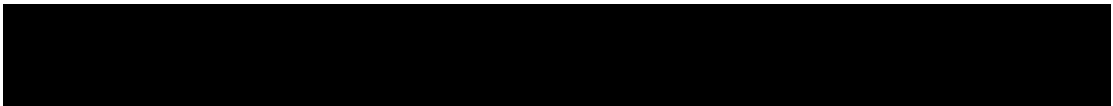


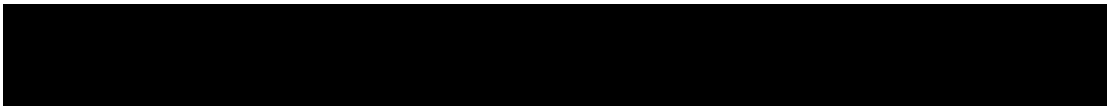


5. Check completion of Write Cycle

Once EEPROM receive a WRITE, WRALL, ERASE or ERAL instruction and recognize a transition from







```
WEN();  
ERASE(0x02); // Erase the data in address 0x02  
PollingEE();  
READ(0x00,(BYTE *)buf_word,8);  
ERAL();
```